Aardvark I²C/SPI Control Center





Supported products:



Control Center User Manual v3.56 February 06, 2014

Summary

The Control Center is a graphical application for use with the Aardvark I²C/SPI Host Adapter. It provides access to all I²C, SPI, and GPIO functions of the Aardvark adapter in an easy-to-use graphical interface. Batch scripting capabillity has been added since version 3.00.



1 Overview

The Aardvark I²C/SPI Embedded Systems Interface is a multi-functional host adapter. The Control Center software interacts directly with the Aardvark adapter.

The Control Center provides basic access to all the functionality of the Aardvark adapter. It is built upon the freely available Aardvark I²C/SPI Software API as detailed in the Aardvark I²C/SPI Embedded Systems Interface manual.

1.1 Changes in version 3.56

New Features

- Added a register read function.
- Added the option to auto connect on launch.
- Added the ability to save user preferences.
- Added the ability to tour the features without an attached Aardvark.

1.2 Changes in version 3.55

Bug Fixes

• Fixed bug with connecting to some Aardvark adapters.

1.3 Changes in version 3.54

New Features

• Added a message when connecting to Aardvark OEM adapters.

1.4 Changes in version 3.53

Bug Fixes

• Fixed an issue with XML i2c_free_bus batch commands causing the GUI to hang.



1.5 Changes in version 3.52

New Features

• XML examples are now included within the GUI package rather than as a separate package.

1.6 Changes in version 3.51

Bug Fixes

• Fixed interoperability issue with FW >= v3.40 when doing 10-bit I^2C reads.

New Features

• Added support for 64-bit Windows. Note that Control Center will run on 64-bit systems as a 32-bit application.

1.7 Aardvark I²C/SPI Host Adapter

The Aardvark I²C/SPI Adapter has 5 core functional modules: I²C, SPI, GPIO, and Batch Scripting.

I²C and SPI are serial protocols for communicating between devices. Information about their specifications can be found in the Aardvark I²C/SPI Embedded Systems Interface manual and on the Total Phase website: http://www.totalphase.com/

GPIO, General Purpose IO, allows the users to take the six pins that are normally used for I²C and SPI and use them to send and receive general signals. These six pins are SCL, SDA, MOSI, SCLK, MISO, and SS. GPIO functionality can be used concurrently with either I²C or SPI, or can be used by itself.

Batch Scripting allows the user to write scripts in an XML based language to automate tasks. The batch scripting functionality in the Control Center software encompasses a subset of the I²C, SPI and GPIO functionality of the Aardvark adapter.

1.8 Aardvark I²C/SPI Host Adapter Functional Modes

The Aardvark I²C/SPI Host Adapter has five functional modes:

1. I²C + SPI



Aardvark 12C/SPI Contro Ble Aardvark Help	l Cente										
I2C Control			Bitrate	Set	400 💌] kHz	SPI Co	ontrol		Bitrate	Set 4000 • kł
Master Stave Slave Add: [0:50] (For Hi Features: [] 10-Bit Add: [] 0 Master Write Message 00 Clear Load Master Read Number of Bytes: [64			7	, _ , _	Free Bu daster W	un	Polarity: Polarity: Plaing/F C Faling/F Master Sla SS Polarity: MOSI Messa 00 01 02 03 12 13 14 15 24 25 26 27 25 27 30 39	P alling (Nising (Nise (* SS Active Los	9 0A 08 0C 0D 0 8 1C 1D 1E 1F 2 2D 2E 2F 30 31 3	High E OF 10 11 0 21 22 23 2 33 34 35	Bit Order: MSB CLSB
Transaction Log						_					
Time	Mod.	R/W	M/S	Feat.	B.R.	Addr	Len.	Data			2
2004-12-03 18:46:24.150	I2C	W	М	5	400	0:(50		00			
2004-12-03 18:46:25.042	I2C	R.	м	5	400	0.:50			E1 E6 52 1A FA		
2004-12-03 18:46:26.243	SP1	W	M	RSML	4000		80		05 06 07 08 09 .		
2004-12-03 18:46:26.243	SPI	R	M	RSML	4000	0.00	80		00 00 00 00 00 .		
2004-12-03 18:46:27.645	12C	R	M	5	400	0::50			C5 9D 81 E8 EF		
2004-12-03 18:46:28.377	I2C	R	M	5	400	0.450			B9 BE 6A A2 33 .		
2004-12-03 18:46:33.033	SPI	W	м	RSML	4000	_	80	00 01 02 03 04	05 06 07 08 09 .		
										Clear L	og Save to File
Port 0 2236-954228	SPI	Write: 0	10								

Figure 1 : I²C and SPI

2. $I^2C + GPIO$

Master Slave Slave Add: 0x50 (For F								Contro				
	lex: ente	r "0x	7	Г	Free B	us	Name Pin#	_	MOSI	SCLK	MISO 5	
Features: 🔲 10-Bit Addr 🥅	Combined P	PMT 🔽	No Stop	p –			Value		0x10	0.08	0x04	
Master Write Message 00					Master V		irection	€ In C Out	€ In C Out	⊂ In ∉ Out	€ In € Out	All In All Out
							Pullups	€ Off € On	(€ Off ⊂ On	C Off	Off On On	All Off All On
							Out Set	t 0	0	0	0	Set
- 1	1 -	1		-			Out Set rt Value		0	0	0	Set
Gear Load Master Read	Sav			×		0.		0	0		. ·	Set Get
	Sav	e R/W	M/S	r .	Master R	01	rt Value	0		0	0	
Master Read Number of Bytes: 64 Transaction Log			M/S			ead 0u	rt Value n Value	0 1 Data	0	0 ×	0	Get
Master Read Number of Bytes: 64 Transaction Log Time 2004-12-03 18:45:53.414 2004-12-03 18:45:55.987	Mod. GPIO GPIO	R/W		Feat.	8.R.	Addr.	rt Value n Value	Data SET: OUT: SET: OUT:	0 0x0a DIRE	0 × <td>0</td> <td>Get</td>	0	Get
Master Read Jumber of Bytes: [64 Tansaction Log Time 2004-12-03 18:45:55.414 2004-12-03 18:45:55.477	Mod. GPIO GPIO 12C	R/W	м	Feat.	8.R. 400	Addr.	n Value	Data Data SET: OUT: SET: OUT:	0 0::0a DIRE 0::02 DIRE	0 × CTION:0x08 CTION:0x08	0 0 0 0 0 0 0 0 0 0 0	Get
Master Read Jumber of Bytes: 64 Transaction Log Time 2004-12-03 18:45:53.414 2004-12-03 18:45:55.997 2004-12-03 18:45:58.913 2004-12-03 18:45:58.913	Mod. GP10 GP10 12C 12C	R/W W R	M	5 5	8.R. 400 400	Addr.	It Value n Value	0 1 Data SET: OUT: SET: OUT: 00 42 92 4C	0 0x0a DIRE 0x02 DIRE 11 AD E1 E0	0 × CTION:0:00 CTION:0:00 5 52 1A FA	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	Get
Master Read Number of Bytes: 64 Transaction Log Time 2004-12-03 18:45:53.414	Mod. GPIO GPIO 12C	R/W	м	Feat.	8.R. 400	Addr.	n Value	0 1 5ET: OUT: 5ET: OUT: 00 61: A6 5F	0 0x0a DIRE 0x0a DIRE 11 AD E1 E0 12 DC C5 9	0 × CTION:0x08 CTION:0x08	0 0 5 PULLUP:01 5 PULLUP:01	Get



3. SPI

Elle Aardvark Help	rol Center										1	- 0 >
SPI Control		Bitrate	Set	4000 💌] kHz	gpio c	ontro	ы				
Rising/Falling	Phase: ● Sample/Setup ○ Setup/Sample		Bit Orde (* MSI (* LSB	в		Name Pin# Value	SDA 3 0x02	SCL 1 0x01				
SS Polarity: SS Active Los MOSI Message 00 01 02 03 04 05 06 07 08 0	19 0A 08 0C 00 0	E OF 10 11		Send		Direction	⊂ln ⊛0ut	⊂ In ∉ Out	All In All Ou	_		
12 13 14 15 16 17 18 19 1A 1 24 25 26 27 28 29 2A 28 2C 2 36 37 38 39 3A 38 3C 3D 3E	20 2E 2F 30 31 3	2 33 34 35	5			Pullups	© Off	© Off	All Of			
48 49 4A 48 4C 4D 4E 4F	JF 4U 41 42 43 4	4 62 60 61					C On	C On	All Or			
	JF 4U 41 4Z 43 4	4 63 65 61	×			Out Set Out Value In Value	C On 1 1 ×	C On O X	All Or Set Get			
	3+ 4U 41 42 43 4	- 00 CP	×			Out Set	1	0	Set			
48 49 44, 48 4C 4D 4E 4F	Save		×			Out Set Out Value In Value	1 1 ×	0	Set			
48 49 44 48 4C 4D 4E 4F Clear Load Transaction Log Time	Save Mod. R/W	M/S	y Feat.	B.R.	Addr.	Out Set Out Value In Value	1 × Deta	0	Get			
Clear Load Transaction Log Trans 2004-12-03 18:44:44.951	Save Mod. R/W SPI W	M/S	Feat.	4000		Out Set Out Value In Value	1 1 × Data 00 01 02 (0 0 ×	Get			
Clear Load Transaction Log Tme 2004-12-03 18:44:44.951 2004-23 18:44:44.951	Save Mod. R/W SPI W SPI R	M/S	y Feat.			Out Set Out Value In Value	1 1 × Data 00 01 02 0 00 00 00	0 0 × 33 04 05 06 00 00 00	Get			
Clear Load Transaction Log Tre 2004-12-03 18:44:44.951 2004-12-03 18:44:451 2004-12-03 18:44:451 2004-12-03 18:44:451 2004-12-03 18:44:551 2004-12-03 18:44:55 2004-12-03 18:44:55 2004-12-03 18:545 2004-12-03 18:45 2004-12-03 18:45 2004-12-03	Save Mod. R/W SPI W SPI R GPIO	M/S	Feat.	4000		Out Set Out Value In Value	1 1 × 00 01 02 0 00 00 00 SET: OUT	0 0 × 33 04 05 06 00 00 00	Get			
Clear Load Transaction Log Time 2004-12-03 18:44:551 2004-12-03 18:44:551 2004-12-03 18:44:551 2004-12-03 18:44:54.206	Save Mod. R/W SPI W SPI R	M/S	Feat.	4000		Out Set Out Value In Value	1 1 × Data 00 00 00 02 SET: OUT GET: IN:0	0 0 × 33 04 05 06 00:00 00 00 00:00 00 00 00:00 00 00 00:00 00 00 00:00 00 00 00:00 00 00:00 00:00 00 00:00 00 00:00 00:00 00 00:00	Get			
48 49 40 48 4C 4D 4E 4F Clear Load Transaction Log	Save Mod. R/W SPI W SPI R GPIO GPIO	M/S M M	Feat. RSML RSML	4000 4000		Out Set Out Value In Value	1 1 × Data 00 01 02 (0 00 00 00 0 SET: 0UT GET: IN:0 00 01 02 (0 00 00 00 0)	0 0 × 33 04 05 06 00 00 00 0-0-0 00 0-0-0 00 0-0-0 00 0-0 00	Get 07 08 09 00 00 00 CTION:0x06 F 07 08 09 00 00 00 07 08 09		0	

Figure 2 : *I*²*C* and GPIO using the 4 available SPI pins: MOSI, SCLK, MISO, SS

Figure 3 : *SPI and GPIO using the 2 available I*² *C pins: SCL, SDA*

4. GPIO Only



5000	ontro	ol											
Name	SS	MOSI	S	CLK	MISO	SD	A E	SCL					
Pin≇	9	8		7	5	3		1					
Value	0x20	0x10	0	M08	0x04	0x0		0.01					
Funce							-		Allin	-			
Direction	🖲 In	🖲 In		In	🖲 In	C Ir		î În 🛛	All In				
onection	C Out	C Out	•	Out	C Out	. 0	lut 🤅	Out	All Out				
			~						AI O!	_			
Pullups	○ 0# C 0n	Off C On		0H Con	Off O	00		© 08 . © On					
	- On	. 011	~				200 1	0.00	All On				
Out Set	0	0		1	0	1		1	Set				
out Value	0	0		1	U.	1		1					
)ut Value In Value	1	0		×	0	×		× [Get	3			
In Value	1	0			0								
In Value ransaction	1 n Log	0	Mod.	R/W		Feat.	B.R.	Addr.	Get	Deta			
In Value Transaction Time 2004-12-03 1	1 1 Log 8:43:23.2	0	Mod. SPIO		0					SET: OUT:0x0a D0			
In Value ransaction Time 2004-12-03 1 2004-12-03 1	1 1 Log 8:43:23.2 8:43:24.2	0	Med. SPIO		0					SET: OUT:0x0a D0 GET: IN:0x20 DIRE	ECTION:0x0b	PULLUP:0x00	
In Value ransaction Time 2004-12-03 1 2004-12-03 1 2004-203 1	1 8:43:23.21 8:43:24.2 8:43:25.0	0	Mod. 3P10 3P10 3P10		0					SET: OUT:0x0a D0 GET: IN:0x20 DIRE GET: IN:0x20 DIRE	CTION:0x0b	PULLUP:0x00 PULLUP:0x00	
In Value ransaction Time 2004-12-03 1 2004-12-03 1 2004-12-03 1 2004-12-03 1	1 8:43:23.21 8:43:24.2 8:43:25.0 8:43:25.0	0 89 (0 30 (0 12 (0 57 (0	Med. SPIO		0					SET: OUT:0x0a D0 GET: IN:0x20 DIRE	CTION:0x0b CTION:0x0b RECTION:0x0	PULLUP:0x00 PULLUP:0x00 0b PULLUP:0x00	
in Value In Value Time 2004-12-03 1 2004-12-03 1 2004-12-03 1 2004-12-03 1 2004-12-03 1	1 8:43:23.21 8:43:24.2 8:43:25.0 8:43:25.1 8:43:30.4	0 89 0 30 0 12 0 60 0	Med. 5P10 5P10 5P10 5P10		0					SET: OUT:0x0a D0 GET: IN:0x20 DIRE GET: IN:0x20 DIRE SET: OUT:0x0b D0	ECTION:0x0b ECTION:0x0b RECTION:0x0 ECTION:0x0b	PULLUP:0x00 PULLUP:0x00 0b PULLUP:0x00 PULLUP:0x00	

Figure 4 : GPIO using the 6 available l²C and SPI pins: SCL, SDA, MOSI, SCLK, MISO, SS

5. Batch Scripting

Batch Instructions											
(aardvak) cconfigure i2c+"1" spi+"1", (i2c_bitate khz+"100"/> (i2c_write add="0x38" cou (i2c_write add="0x38" cou (i2c_write add="0x38" cou (i2c_write add="10x38" cou (i2c_write add="10x38" cou (i2c_write add="10x38" cou	nt="0" rac nt="2" rac nt="2" rac nt="2" rac	for=''16''> for=''16''> for=''16''> for=''16''>	20_w<br 03 00 <br 01 fe 2</th <th>nite> 12c_write) 2c_write> 2c_write></th> <th>•</th> <th></th> <th></th> <th></th> <th></th> <th></th> <th>Stopped Execute Stop Help</th>	nite> 12c_write) 2c_write> 2c_write>	•						Stopped Execute Stop Help
Calego mon"70"7> <i2c_write add="0x38" cou<br="">calego mon"70"7> <i2c_write add="10x38" cou<br="">calego mon"70"7> <i2c_write add="10x38" cou<br="">calego mon"70"7> <i2c_write add="10x38" cou<br=""><ale calego="" cou<br="">cou c</ale></i2c_write></i2c_write></i2c_write></i2c_write>	nt="2" rad nt="2" rad	for="16"> for="16">	01 e0 </th <th>2c_write 2c_write</th> <th>></th> <th></th> <th></th> <th></th> <th></th> <th>-</th> <th></th>	2c_write 2c_write	>					-	
<i2c_write add="0x38" cou<br="">caleep ma="70"/> <i2c_write add="0x38" cou<br="">caleep ma="70"/> <i2c_write add="0x38" cou<br="">caleep ma="70"/> <i2c_write add="0x38" cou<br="">caleep ma="70"/></i2c_write></i2c_write></i2c_write></i2c_write>	nt="2" rad nt="2" rad	for="16"> for="16"> for="16">	01 e0 </th <th>2c_write 2c_write</th> <th>></th> <th></th> <th></th> <th></th> <th></th> <th>×</th> <th></th>	2c_write 2c_write	>					×	
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(i2c_white add="0xi3" could called a to initial called a to ini	nt="2" rac nt="2" rac nt="2" rac Sav	6x="16"> 6x="16"> 6x="16"> 0x="16">	01 e0c/ 01 e0c/ 01 80c/	2c_write 2c_write 2c_write 2c_write	B.R.	Addr.	Len.	Deta		×	
Ci2c_write add="0x38" co. Caleep ma="70"/> Ci2c_write add="0x38" co. Ciaep ma="70"/> Ciaep ma="70"/> Ciaep ma="70"/> Ciaep ma="70"/> Time 2004-12-03 18:42:34.756	nt="2" rac nt="2" rac nt="2" rac Sav Mod. 12C	6x=''16''> 6x=''16''> 6x=''16''> 8 8 8 8 8 8 8 9 9 9 9 9 9 9 9 9 9 9 9	01 e0c/ 01 e0c/ 01 80c/ 01 80c/ M/S	Zc_write Zc_write Zc_write Zc_write	B.R. 100	0x38	2	01 07		×	
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ci2c_write add="0x38" could called pina="707"/> ci2c_write add="0x38" could pina="707"/> ci2c_write add= add= add= add= add= add= add= add	#R="2" tec #R="2" tec #R="2" tec Sav Sav Mod. 12C 12C	6x=''16'> 6x=''16'> 6x=''16'> 6x=''16'> W W W W W	01 e0c/ 01 e0c/ 01 80c/ M/S M M	Co_write Co_write Co_write Co_write	B.R. 100 100	0x38 0x38 0x38	2 2 2	01 07 01 0F 01 1F		*	[
(Cc, write add="00:38" co. (cleep mex"707:/> (Cc, write add="10:38" co. (cleep mex"707:/> (Cc, write add="10:38" co. (cleep mex"707:/> (Cc, write add="10:38" co. (cleep mex"707:/> Clear Load Tansaction Log Time 200+12-03 18:42:34.836 200+12-03 18:42:34.917 200+12-03 18:42:34.9197	nt="2" rac nt="2" rac nt="2" rac Sav Sav Mod. 12C 12C 12C 12C	6x=''16''> 6x=''16''> 6x=''16''> 6x=''16''> 8 8 8 8 8 8 8 9 9 9 9 9 9 9 9 9 9 9 9	01 e0c/ 01 e0c/ 01 80c/ M/S M	C_write Cc_write Cc_write Peat.	B.R. 100 100 100	0x38 0x38 0x38 0x38	2 2 2 2	01 07 01 0F 01 1F 01 3F		1	
ci2c_write add="0x38" cou- csleep ma="70"/> ci2c_write add="0x38" cou- csleep ma="70"/> ci2c_write add="0x38" cou- csleep ma="70"/> ci2c_write add="0x38" cou- csleep ma="70"/>	#R="2" tec #R="2" tec #R="2" tec Sav Sav Mod. 12C 12C	6x=''16'> 6x=''16'> 6x=''16'> 6x=''16'> W W W W W W W W W	01 e0c/ 01 e0c/ 01 80c/ 01 80c/ M M M M	Ec_write Cc_write Cc_write Cc_write	B.R. 100 100	0x38 0x38 0x38	2 2 2	01 07 01 0F 01 1F		1	



Figure 5 : Batch Scripting Module



2 Getting Started

2.1 Requirements

2.1.1 Overview

The Control Center software is offered as a 32-bit binary and it is compatible with 64-bit operating systems. The following sections describe the requirements to run Control Center. Be sure the device driver has been installed before plugging in the Aardvark adapter. Refer to the Software section of the Aardvark manual for additional information regarding the driver and compatibility.

2.1.2 Windows

The Control Center software is compatible with 32-bit and 64-bit versions of Windows XP (SP2 or later), Windows Vista, Windows 7, and Windows 8/8.1. The software will run on 64-bit systems as a 32-bit application. Windows 2000 and legacy 16-bit Windows 95/98/ ME operating systems are not supported.

2.1.3 Linux

The Control Center software has been designed for Red Hat Enterprise Linux 5 with integrated USB support. Kernel 2.6 is required. Other distributions, including Ubuntu, Fedora, CentOS, SuSE, Debian, and Arch have also been known to work. The software will run on 64-bit systems as a 32-bit application, provided that 32-bit system libraries are available.

2.1.4 Mac OS X

The Control Center software is compatible with Intel versions of Mac OS X 10.5 Leopard, 10.6 Snow Leopard, 10.7 Lion, 10.8 Mountain Lion, and 10.9 Mavericks. Installation of the latest available update is recommended.

2.1.5 Aardvark I²C/SPI Host Adapter

Version 3.50 of the Control Center software requires an Aardvark adapter that runs version 3.40 or greater of the firmware.

Aardvark adapters with version 2.1 or greater of the hardware will be able to use the target power and configure I²C pullup functionality.



2.2 Installing Control Center

The Control Center software is a self-contained application. Installing the software is as easy as unarchiving the software package. To install the Control Center:

- 1. Download the latest version of the software from the Total Phase website.
- 2. Unzip the zip archive to your desired location.

2.3 Launching the Control Center

2.3.1 Windows

🔁 C:\gui-v2.	00			- 🗆 🗵
<u> </u>	<u>V</u> iew F <u>a</u> vorit	es <u>T</u> ools	Help	11
] 🗇 Back 👻	⇒ - 🖻 🖗	Search 🖻	Folders	»
Address	C:\gui-v2.00		•	<i>i</i> ∂Go
		5Å		
images	lib	Aardvark GUI.exe	LICENSE.txt	
4 object(s)	20.8 KB	۱ 🖳	My Computer	

Figure 6 : Control Center Directory – This is what the directory should look like after the software has been extracted.

- 1. Go to the folder where the software package was extracted.
- 2. Click on "Aardvark GUI.exe"

2.3.2 Linux

1. Go to the installation directory where the software package was unzipped.



2. Run >./aardvark

2.3.3 Mac OS X

- 1. Go to the installation directory where the software package was unzipped.
- 2. Click on "Aardvark GUI"

2.4 Operating the Control Center

An Aardvark adapter must be configured for use before the Control Center software can be used to send and receive any messages. To configure an Aardvark adapter, go to the menu item: **Aardvark** | **Connect**.

2.4.1 Configure the Aardvark Adapter

of Confi	gure Aaı	dvark A	dapter					×
Config	gure A	ardva	r <mark>k Ada</mark>	pter				
Select	an Aaro	lvark p	ort					Select a Mode
Port	FW	HW	I2C	SPI	GPIO	Serial Number		I2C - SPI
0	3.50	3.00	Y	Y	Y			C I2C - GPIO C SPI - GPIO
								C GPIO Only
								C Batch Mode
Refresh	n List							
							ОК	Cancel

Figure 7 : Configure Aardvark Adapter Window – The Configure window is organized in two major sections: the list of available Aardvark adapters and the list of operational modes.

The configuration window (figure 7) is organized into two sections: the list of available Aardvark adapters and a list of operational modes.



List of Available Aardvark Adapters

In the configure window, there is a list of all the available Aardvark adapters that are connected to the computer. If no Aardvark adapters appear in the list, then there are no available units connected to your computer.

Refresh List

To see an updated list of Aardvark adapters attached to the computer, simply click on the **"Refresh List"** button to rescan the USB bus. Please note that Aardvark adapters that are in use by other applications are no longer available and consequently do not appear in the list of available units.

The list of Aardvark adapters provides the following information:

Port

The port that the Aardvark adapter occupies. The port number is a zero based number. For more information about USB port assignments, please consulting section *3.4: USB Port Assignment* of the Aardvark I²C/SPI Host Adapter manual.

Hardware Version (HW Ver.) & Firmware Version (FW Ver.)

For more information about version numbers, please consult the Aardvark I²C/SPI Embedded Systems Interface manual, *Sections 3.5 Aardvark Dynamically Linked Library* – *Versioning* and *3.6 API Integration into Custom Applications* – *Versioning*.

Serial Number

The serial number of the Aardvark adapter.

I²C, SPI, and GPIO

Supported modules. "Yes" indicates that a module is supported. "No" indicates that a module is not supported.

2.4.2 Operational Modes

On the right side of the window is a list of the six operational modes: "I²C + SPI", "I²C + GPIO", "SPI + GPIO", "GPIO Only", and "Batch Mode". Select the radio button next to the desired mode.

The mode of the Aardvark adapter can be changed after the unit has already been connected, see *Section 2.3 Reconfiguing the Aardvark Adapter*.



Once you have selected the Aardvark adapter and the desired mode, click on **"OK"**" to continue.

The port and serial number of the Aardvark adapter will appear in the status bar at the bottom of the window to indicate which Aardvark adapter is bound to this instance of the application.

2.5 Reconfiguring the Aardvark Adapter

After the Control Center has been configured with a specific Aardvark adapter, it is possible to change the Aardvark adapter and/or the mode that it is operating in.

2.5.1 Change Mode

To change the mode but continue using the same Aardvark adapter go to the menu item:

Aardvark

Select the desired mode and the Control Center will be automatically reconfigured to reflect this selection.

2.6 Powering Downstream Devices

It is possible to power a downstream target, such as an I²C or SPI EEPROM with the Aardvark adapters power (which is provided by the USB port). More information about powering downstream devices can be found in the Aardvark I²C/SPI Embedded Systems Interface manual.

NC/+5V (Pin 4) : I²C Power

NC/+5V (Pin 6) : SPI Power

By default, these pins are left unconnected at the time of shipping. For Aardvark adapters with hardware versions 2.00 and greater, these pins can be enabled through the Control Center software. Simply go to the menu item: **Aardvark | Target Power**.

A checkmark indicates that power will be supplied to downstream devices on both pins.

2.7 Disconnecting the Aardvark Adapter

The Aardvark adapter can be disconnected from the current application. To do so, go to the menu item: **Aardvark** | **Disconnect**.

When disconnected, the application will return to the starting screen.



2.8 Auto Connect the Aardvark Adapter

The **Auto Connect** option allows single-analyzer users to conveniently bypass the device connection during Control Center launch. When this option is enabled and one analyzer is attached to the system, Control Center will automatically connect to the analyzer on launch. To do so, go to the menu item: **Aardvark | Auto Connect**

Note: Auto connect may fail if the device is in use by another application or is otherwise not connectable. In this case, a warning will appear alerting the user of the connection failure.

2.9 Exiting the Application

To exit the application, go to the menu item: File | Exit Application.



3 Application

3.1 General

The main application window is divided into two sections. The top section contains the modules for use with the Aardvark adapter. Depending on the selected mode, different modules will appear in the main display. The available modules are I^2C , SPI and GPIO. Each one has different features and functionality which are explained in *Section 4. Modules*.

The bottom section of the application contains the Transaction Log. The log keeps track of all transactions that the Aardvark adapter sends or receives.

3.2 Transaction Log

Time	Mod.	R/W	M/S	Feat.	B.R.	Addr.	Len.	Data
2004-12-03 18:47:46.733	I2C	W	M	S	400	0x50	1	00
2004-12-03 18:47:48.617	I2C	R	M	S	400	0x50	64	42 92 4C 11 AD E1 E6 52 1A FA
2004-12-03 18:47:49.769	SPI	W	M	RSML	4000		80	00 01 02 03 04 05 06 07 08 09
2004-12-03 18:47:49.769	SPI	R	M	RSML	4000		80	00 00 00 00 00 00 00 00 00 00
2004-12-03 18:47:50.569	I2C	R	M	S	400	0x50	64	B1 A6 5F 1E DC C5 9D 81 E8 EF
2004-12-03 18:47:50.819	I2C	R	M	S	400	0x50	64	37 94 B7 73 7B B9 BE 6A A2 33
2004-12-03 18:47:51.260	I2C	R	M	S	400	0x50	64	7D 2C 2F CF AF 67 F9 1F 54 A1
2004-12-03 18:47:54.825	SPI	W	М	RSML	4000		80	00 01 02 03 04 05 06 07 08 09
2004-12-03 18:47:54.825	SPI	R	M	RSML	4000		80	00 00 00 00 00 00 00 00 00 00
2004-12-03 18:48:02.407	GPIO							SET: OUT:0x02 DIRECTION:0x00 PULLUP:0x00
2004-12-03 18:48:05.743	GPIO							SET: OUT:0x03 DIRECTION:0x00 PULLUP:0x00
2004-12-03 18:48:12.403	GPIO							GET: IN:0x23 DIRECTION:0x00 PULLUP:0x00
2004-12-03 18:48:18.262	SPI	W	M	RSML	4000		80	00 01 02 03 04 05 06 07 08 09
2004-12-03 18:48:18.262	SPI	R	M	RSML	4000		80	00 00 00 00 00 00 00 00 00 00
2004-12-03 18:48:21.355	I2C	W	M	S	400	0x50	1	00
2004-12-03 18:48:22.046	I2C	R	M	S	400	0x50	64	42 92 4C 11 AD E1 E6 52 1A FA
2004-12-03 18-48-29 608	120	R	M	S	400	0x50	64	81 46 SE 1E DC CS 9D 81 E8 EE

3.2.1 Elements of the Transaction Log

Figure 8 : The Transaction Log – The Transaction Log records all transactions that are performed by the Aardvark adapter. This log can be saved as a file for future reference.

The Transaction log is a scrolling list of all the transactions that the Aardvark adapter sends or receives. The information is arranged in the following columns.

Time

Time of the transaction. This information is displayed in the format:



YYYY-MM-DD hh:mm:ss.xxx

Module (Mod.)

The module that logged the transaction. This can be either *I*²*C*, *SPI*, or *GPIO*. Log entries from for different modules have different background colors to make them easier to identify.

Table 1 : Transaction Background Colors

I ² C	White
SPI	Yellow
GPIO	Gray

Read/Write (R/W)

This column is only used by l^2C and SPI

Whether the transaction was a read transaction ("**R**") or a write transaction ("**W**") or a register read transaction ("**RegW**" or "**RegR**").

Master/Slave (M/S)

This column is only used by I²C and SPI

Indicates the mode that the attached Aardvark adapter is functioning in. If the Aardvark unit is operating as a master, then an "M" appears. In the Aardvark unit is operating as a slave, then an, "S" appears.

Features (Feat.)

This column is only used by I²C and SPI

This is a string that indicates the features that were active during the transaction.

l²C

I²C features are encoded in the string: TCS

- T 10-bit Addressing
- C Combined FMT
- S No Stop



If the feature is not being used, then a "-" appears in place of the character. More information about the specific features can be found in *Section 4.1 I^2C*.

For example, in a transaction that uses 10-bit addressing and the no stop feature, the feature string would be "T-S".

SPI

SPI data exchange parameters are string encoded. The order of the parameters is:

```
Polarity ("R" or "F")
```

Rising ("R") or Falling ("F")

Phase ("S" or "s")

Sample/Setup ("S") or Setup/Sample ("s")

```
Bit Order ("M" or "L")
```

Most Significant Bit First - MSB ("M) Least Significant Bit First - LSB ("L")

SS Polarity ("L" or "H")

SS Active Low ("L") SS Active High ("H")

If a feature is not being used, then a "-" appears in the place of the character. More information about the SPI Bit Protocol can be found in *Section 4.2 SPI*

For example, in a transaction that uses Falling polarity, Setup/Sample Phase and Most Significant Bit First, the feature string would be: "**FsM-**"

Bitrate (B.R.)

This column is only used by I²C and SPI

Indicates the speed of the transaction in kbps. The value logged in this column many differ from the bitrate specified. The value reported here is the actual bit rate returned by the Aardvark adapter.

Address (Addr.)

This column is only used by l^2C

The address that was the target of the transaction.



In the case of I^2C master, the address is the slave address that the master targeted for communication.

In the case of I²C slave, the address will be the address of the Aardvark adapter unless the Aardvark slave is answering a general call in which case 0x80 will appear in the address column.

Length (Len.)

This column is only used by I²C and SPI

The number of bytes in the transaction. In the case where bytes are dropped from the transmission, DB will appear in this column to indicate that the message was truncated and bytes were dropped from the end of the message.

Data

I²C and SPI

The message in hexadecimal.

GPIO

The GPIO values in the log is the OR-ed values of the pins involved. The pins have the following values:

 $SCL = 0 \times 01$ $SDA = 0 \times 02$ $MIS0 = 0 \times 04$ $SCK = 0 \times 08$ $MOSI = 0 \times 10$ $SS = 0 \times 20$

Set

The data is in the format:

OUT:0xXX DIRECTION:0xXX PULLUPS:0xXX

Get

The data is in the format:



IN:0xXX DIRECTION:0xXX PULLUPS:0xXX

OUT and IN

1 indicates that the pin has been set logic high and 0 indicated logic low. For example, if **SCL**, **MOSI** and **SS** where set to logic high, then the value of OUT would be: 0x31.

 $0 \times 01 \mid 0 \times 10 \mid 0 \times 20 = 0 \times 31$

DIRECTION

1 indicates that the direction is out and 0 indicates that the direction is in. For example, if **SDA**, **MISO** and **SS** were set to output and the rest of the pins to input, the value of DIRECTION would be: 0x26.

 $0 \times 02 \mid 0 \times 04 \mid 0 \times 20 = 0 \times 26$

PULLUPS

1 indicates that the pullup is on and 0 indicates the pullup is off. For example, if **MISO**, **SCK**, **MOSI** and **SS** had their pullups turned on, the value of PULLUP would be: 0x3c.

 $0 \times 04 \mid 0 \times 08 \mid 0 \times 10 \mid 0 \times 20 = 0 \times 3c$

More information about GPIO can be found in Section 4.3 GPIO.



3.2.2 Transaction Viewer

Mansaction	Viewer
Time:	2003-11-19 14:28:15.202
Module:	SPI
R/W:	R
M/S:	S
Features:	FSM-
Bit Rate:	
Slave Addr:	
Length:	16
Data:	FF FE FD FC FB FA F9 F8 F7 F6 F5 F4 F3 F2 F1 F0
	Save Data Close

Figure 9 : The Transaction Viewer

The transaction viewer is a convenient way to view the full details of a transaction. To see an transaction in the Transaction Viewer, double-click on an entry in the transaction log.

Save Data

The transaction data can be saved in a binary file. Click on the **"Save Data"** button to bring up the save file dialog. This binary file can later be loaded as a message in I²C or SPI.

3.2.3 Clear Log

Deletes all entries from the Transaction Log.

Note that all transactions are cleared immediately when the button is pressed.



3.2.4 Save To File

The data in the log can be exported in a comma separated values (CSV) format by clicking on the **"Save To File"** button. You will be prompted for a filename to save the data.

The log file has a header with the following information:

```
Export Time: [time of export]
Port [port number]
Aardvark HW_Version: [hw version]
FW_Version: [fw version]
```

After the header, all transactions are appended, one per line. The column order in the exported file is the same as the order columns in the transaction log.



4 Modules

4.1 l²C

Inter-IC bus, or I²C, was developed by Philips in the 1980s. I²C is a low-bandwidth, short distance protocol for on board communications. All devices are connected through two wires: serial data (SDA) and serial clock (SCL).

The Control Center I²C module consists of 2 tabs, master and slave.

4.1.1 I²C Pull-ups

There is a 2.2 K resistor on each I²C line (SCL, SDA). The lines are effectively pulled up to 3.3 V, so that results in approximately 1.5 mA of pull-up current. For more information about the pull-up resistors, please consult the Aardvark I²C/SPI Embedded Systems Interface manual.

Aardvark adapters with a hardware versions 2.00 and greater have the ability to enable the pull-up resistors through the Control Center application. To toggle the pull-up resistors, go to the menu item:

Aardvark -> I²C Pull-ups

A checkmark indicates that the pull-up resistors have been enabled on the I²C lines.



4.1.2 I²C Master

Master Slave	
Slave Addr: 0x63 (For Hex: enter "0x")	Free Bus
Features: 🔽 10-Bit Addr 🔲 Combined FMT 🔲 No Stop	
Master Write	
Message	
00 11 22 33 44 55 66 77 88 99 AA BB CC DD EE FF	Master Write
Clear Load Save	
Master Read	
Number of Data Bytes: 16	Master Read
Master Register Read	
Register Address: 0x1234 Address Width: 2 bytes	•
Number of Data Bytes: 12 Master	Register Read

Figure 10 : l^2C Master tab of the l^2C Module

As a master device, there are three actions, write, read, or register read. For these actions, there are a number of parameters that can or must be specified: bitrate, slave address, and other I^2C features.

Bitrate

The bitrate is the speed of communications between the master and the slave. The maximum master I²C bitrate is 800 kbps and a minimum of 1 kbps. The Aardvark I²C module supports many intermediate bit rates between these values. More information about the bit rate can be found in the Aardvark I²C/SPI Embedded Systems Interface user manual.

Changing the Bitrate

To change the bitrate, simply select a bitrate from the pull-down menu or alternatively, enter your own bitrate and press **<Enter>**.





Figure 11 : Bitrate Confirmation Dialog

If the bitrate you entered is not available, the application will display a message indicating the close matching bitrate. Click on **"Yes** "to accept this alternative bit rate, or click on **"No"** to continue using the existing bitrate.

Slave Address

The slave address is the address of the target I²C slave device. This address can be entered in either decimal or hexadecimal notation. If using hexadecimal notation, preface the number with "0x". For 7-bit and 10-bit addressing, the 7 and 10 least significant bits should be used to specify the address, respectively.

Features

The Aardvark adapter supports many of the additional I²C features.

10-Bit Slave Address (10-Bit Addr.)

When 10-bit slave address is selected, the slave address will be treated as a 10-bit address. The appropriate actions as described in the I^2C specification will be performed to address the 10-bit slave on the bus.

Note: The Aardvark slave is always a 7-bit addressed device.

Combined Format (Combined FMT)

When Combined Format is selected, the "combined" format will be used for **Master Read** commands. This feature is only enabled when 10-bit addressing is active because it is only useful when used in conjunction with 10-bit slave Addressing

This flag indicates to the Aardvark adapter that the address is a 10-bit address but that it is not necessary to send the entire address using a master write before executing the read.

For specific information about the "combined" format, consult *Section 14.2 "Formats with 10-bit address"* in the Philips I²C Specification. A link to the specification can be found on the Total Phase website.



No Stop (No Stop)

When No Stop is selected, the master device will explicitly not signal the stop command after the last byte in a transaction. The bus will be held and the subsequent master read or master write events will issue the repeated start on the bus.

Free Bus

The "Free Bus" button will explicitly issue the stop command on the I²C bus.

If the Aardvark I²C subsystem had executed a master transaction and is holding the bus due to a previous "No Stop" transaction, the Free Bus command will issue the stop command and free the bus.

I²C Master Write

Message to Send

Enter the message to be sent in hexadecimal in this field. Spaces will be automatically added for better legibility but these spaces will not be sent as part of the message. The maximum message size is 64k (65535) bytes when using 7-bit addressing and 64k-1 (65534) bytes when using 10-bit addressing.

The message can be loaded from a binary file by clicking on the **"Load"**. Conversely, the message can also be saved to a binary file by clicking on the **"Save"** button.

Once a message has been set, click on the **"Master Write"** button to initiate the action. The results of the action will appear in the transaction log.

I²C Master Read

Number of Data Bytes

This value is the maximum number of bytes the master will accept in a single transaction. The master may receive fewer bytes than are specified in this field, but not more. In the case that a slave does not have the requested number of bytes available, the remainder of the bytes will simply default to 0xff due to the pullup resistors on the bus.

I²C Master Register Read

The typical protocol to read a register on an I2C device is to perform an I2C write with the register address followed by a repeated start and an I2C read. The Master Register Read feature provides a way to do this in one operation. Please consult the datasheet of your I2C slave device to ensure it follows this protocol.



Register Address

This register address can be entered in either decimal or hexadecimal notation. If using hexadecimal notation, preface the number with "0x". Note that this is different from the I^2 C slave address.

Address Width

The Address Width specifies the size in bytes of the register address. If the provided Register Address exceeds this width, the least significant bytes of the Register Address are used.

Number of Data Bytes

This value is the number of bytes the adapter will attempt to read from the I^2C slave. The adapter may receive fewer bytes than are specified in this field, but not more. In the case that a slave does not have the requested number of bytes available, the remainder of the bytes will simply default to 0xff due to the pullup resistors on the bus.

4.1.3 I²C Slave

I2C Con	trol		Bit	trate 💶 🔽 💌 Khz
Master Slave				
Current Stat	tus:			Disabled
Slave Addr: 95	(For H	ex: enter "0x.	")	Enable
Max Tx Bytes:	0 Max	Rx Bytes: 0		
(0 indicates a	an unlimited r	number of by	tes)	
Message				
	AB CD EF 01 2 CD EF 01 23 4			Set Resp.
			-	1
Clear	Load	Save		



Figure 12 : PC Slave tab of the PC Module

Slave Enable

An I²C slave can send messages to and receive messages from a master device after the master has initiated a transaction. An Aardvark adapter will not respond as an I²C slave device unless it has been enabled as a slave device.

When enabling an Aardvark adapter as a slave device, three parameters must be provided: the "Slave Address", the maximum number of bytes to send ("Max Tx Bytes") and the maximum number of bytes to receive ("Max Rx Bytes").

Slave Address

This is the I²C address that the Aardvark adapter will use as an I²C slave device. The Aardvark adapter always uses a 7-bit slave address. The address is specified in the 7 least significant bits. The most significant bit is ignored.

Max Tx Bytes & Max Rx Bytes

The Max Tx Bytes and Max Rx Bytes indicates the maximum number of bytes the Aardvark device will send and receive respectively. The Aardvark adapter will not exceed the maximum number of bytes that have been specified.

An exception to this rule is "0" which indicates that the number of bytes is unlimited.

Slave Response

A slave response message can be set in the Aardvark adapter as a response to a write request. The message entry field operates in the same manner as the I²C master message to send field. The maximum message size is 64 bytes due to buffer limits.

The message can be loaded from a binary file by clicking on the **"Load"** button. Conversely, the message can also be saved to a binary file by clicking on the **"Save"** button.

If more bytes are requested in a transaction than have been specified in the slave response, the response string will be wrapped as many times as necessary to complete the transaction. For example if the slave response has been set to:

00 01 02 03 04

and 12 bytes have been requested, the response that is sent to the master will be:



$00 \ 01 \ 02 \ 03 \ 04 \ 00 \ 01 \ 02 \ 03 \ 04 \ 00 \ 01$

To set the response in the slave, click on the "Set Resp" button. It is advisable to set the slave response before enabling the slave. If a response is not set before the slave is enabled, it is possible that a slave response be requested before the slave device has one to return.

Note: Slave Message Can Be Overwritten

All I²C messages share memory in the Aardvark adapter. Therefore it is possible that the slave response may be overwritten in the Aardvark unit.

For example, an I^2C slave response is set and then Aardvark adapter is enabled as an I^2 C slave, but then without disabling the slave, an I^2C master transaction is executed. The Aardvark adapter will implicitly deactivate the I^2C slave and because of the shared memory in the Aardvark adapter the I^2C master operation will almost always overwrite the I^2C slave response.

The safest course of action is to set the slave response each time before enabling or reenabling the l^2C slave.

Enabling the Slave

To enable the Aardvark adapter as an I²C slave, simply click on the "Enable" button.

I2C Control	Bitrate 💶 🖌 Khz
Master Slave	
Current Status:	Enabled
Slave Addr: 95 (For Hex: enter "0x")	Disable
Max Tx Bytes: 0 Max Rx Bytes: 0	
(0 indicates an unlimited number of bytes)	
Message	
01 23 45 67 89 AB CD EF 01 23 45 67 89 AB CD EF 01 23 45 67 89 AB CD EF 01 23 45 67 89 AB CD EF	Set Resp.
	v
Clear Load Save	



Figure 13 : Enabled I²C Slave tab of the I²C Module

Once the slave is enabled, the status indicator at the top of the panel will change from "**Disabled**" in red to "**Enabled**" in green.

As request arrive for the slave, the transaction log will be updated with the read and write actions that the slave performed.

Disabling the Slave

To disable the Aardvark adapter as an I²C slave device, simply click on the **"Disable"** button. Once disabled the status indicator at the top of the panel will change from **"Enabled"** in Green to **"Disabled"** in Red.

Implicit Slave Disabling

Executing an I^2C master write or I^2C master read will implicitly disable the Aardvark device as an I^2C slave device. After a master write or read event, it is recommended that the I^2C slave response be resent to the Aardvark adapter and then it can be re-enabled as an I^2C slave device.

Switching the I²C pins to GPIO will also implicitly disable the I²C slave.

4.2 SPI

SPI is a serial communication bus developed by Motorola. It is a full-duplex protocol which functions on a master-slave paradigm that is ideally suited to data streaming applications.

The SPI tab consists of two tabs: master and slave.

4.2.1 SPI Data Exchange Parameters

The SPI master and slave need to agree about the data frame for the transaction. The data frame is described by three parameters: clock polarity, clock phase and bit order.

SPI Control		Bitrate 4000 💌 Khz
Polarity:	Phase:	Bit Order:
C Rising/Falling	Sample/Setup	MSB
• Falling/Rising	Setup/Sample	LSB

Figure 14 : SPI Parameters in the SPI Module



These parameters must be the same for both the master and slave modes. More information about these parameters can be found in the Aardvark I²C/SPI Embedded Systems Interface manual.

Bitrate

The bitrate is the speed of communications between the master and the slave. The Aardvark SPI master can operate at bitrates of 125 kHz, 250 kHz, 500 kHz, 1 MHz, 2 MHz, 4 MHz, and 8 MHz. The quoted bitrates are only achievable within each individual byte and does not extend across bytes. More information about the bit rate can be found in the Aardvark I²C/SPI Embedded Systems Interface manual.

Changing the Bitrate

To change the bitrate, simply select a bitrate from the pull-down menu or alternatively, enter your own bitrate and press **<Enter>**.



Figure 15 : Bitrate Confirmation Dialog

If the bitrate you entered is not available, the application will display a message indicating the close matching bitrate. Click on "**Yes**" to accept this alternative bit rate or click on "**No**" to continue using the existing bitrate.





4.2.2 SPI Master

SPI Control		Bitrate 🚺 🔽 Khz
Polarity: C Rising/Falling C Falling/Rising	Phase: © Sample/Setup © Setup/Sample	Bit Order: ● MSB ● LSB
Master Slave		
SS Polarity: SS Active L MOSI Message	.ow 🔘 SS Active High	
FF EE DD CC BB AA 99 88	77 66 55 44 33 22 11 00	Send
Clear Load	Save	

Figure 16 : SPI Master tab in the SPI Module

The SPI master has an additional parameter that can be set, the polarity of the SS line.

SS Polarity

The SS Polarity indicates whether the Aardvark device will pull the SS pin high or low to activate the SPI slave device.

Note: When configured as an SPI slave, the Aardvark will always be setup with SS as active low.

MOSI Message

MOSI (Master Out, Slave In) message is entered here in hexadecimal format. Spaces are automatically inserted for legibility. The maximum message size is 4 KiB due to operating system buffer limits.

The message can be loaded from a binary file by clicking on the **"Load"** button. Conversely, the message can also be saved to a binary file by clicking on the **"Save"** button.



SPI is a full duplex protocol. When the MOSI message is sent a MISO message is received. The transaction log will log MOSI and MISO as two separate transactions that occur at the same time. The length of the two messages will be the same due to the duplex nature of the protocol.

4.2.3 SPI Slave



Figure 17 : SPI Slave tab in the SPI Module

MISO Message

The MISO (Master In, Slave Out) message is the message that the Aardvark adapter will return as its response to a SPI transaction. Like the MOSI message, this message is entered in hexadecimal format. Spaces are automatically inserted for legibility, but are not sent in the transaction. The maximum message size is 4 KiB due to operating system buffer limits.

It is advisable to set the MISO message before enabling the slave. If a MISO message is not set before the slave is enabled, it is possible that the message may be requested before the slave device has one to return.

The message can be loaded from a binary file by clicking on the **"Load"** button. Conversely, the message can also be saved to a binary file by clicking on the **"Save"** button.



Note: MISO Message Can Be Overwritten

All SPI messages share memory in the Aardvark adapter. Therefore it is possible that the MISO message may be overwritten in the Aardvark unit.

For example, a MISO message is set and then Aardvark adapter is enabled as an SPI slave, but then without disabling the slave, an SPI master transaction is executed. The Aardvark adapter will implicitly deactivate the SPI slave and because of the shared memory in the Aardvark adapter the SPI master operation will almost always overwrite the MISO message.

The safest course of action is to set the MISO message each time before enabling or reenabling the SPI slave.

Slave Enable

An Aardvark adapter will not respond as an SPI slave device until it has been enabled. It is advisable that the MISO message be set in the slave device before it is enabled to ensure valid data to all requests.

SPI Cor	ntrol		Bitra	ate 🚺 🔽 Khz
Polarity: © Rising/Fallir © Falling/Risir Master Slave	ng (• ng (•	hase: Sample/Setup Setup/Sample		Order: MSB LSB
Current Stat				Enabled Disable
		DC BA 98 76 54 BA 98 76 54 32		Set MISO
Clear	Load	Save		

Figure 18 : Enabled SPI Slave tab in the SPI Module

Once the slave is enabled, the status indicator at the top of the panel will change from **"Disabled"** in red to **"Enabled"** in green. As requests arrive for the slave, the transaction log will be updated with the read and write actions that the slave performed.



When the MOSI message is received a MISO message is sent to the master. The transaction log will log MOSI and MISO as two separate transactions that occur at the same time.

Implicit Slave Disabling

Executing a SPI master write will implicitly disable the Aardvark device as an SPI slave device. After a master write event, it is recommended that the MISO message be resent to the Aardvark adapter and then it can be re-enabled as an SPI slave device.

Switching the SPI pins to GPIO will also implicitly disable the SPI slave.

GPIO Control SS MOSE Name SCLK MISO SDA SCL Pin# 9 8 7 5 3 1 0x20 0x04 0x02 Value 0x10 0x08 0x01 All In O In 🖲 In . 🖲 In ◯ In 💿 In ⊖ In Direction Out 🖲 Out 🔘 Out O Out 🖲 Out 🔘 Out All Out All Off Off 🖸 Off Off Off. 🔿 Off 🖸 Off Pullups \sim O On 🔘 0n 🔘 0n 🖸 0n. 🔘 0n All On Out Set 1 1 0 0 0 0 Set Out Value 0 1 0 In Value Get X X 1 0 X 1

4.3 General Purpose IO



General Purpose IO, GPIO, allows the users to use the six pins that are normally used for I²C and SPI and use them to send and receive signals. These six pins are SCL, SDA, MOSI, SCLK, MISO, and SS. GPIO functionality can be combined with either I²C or SPI or can be used by itself.

4.3.1 GPIO Configurations

When GPIO is combined with either I^2C or SPI, only the pins of the unused module are available for GPIO. Therefore when using " $I^2C + GPIO$ " only the SPI pins are available for GPIO and when using "SPI + GPIO", only the I^2C pins are available for GPIO.



GPIO Control

Name	SS	MOSI	SCLK	MISO	
Pin#	9	8	7	5	
Value	0x20	0x10	0x08	0x04	
Direction	⊂ In ∙ Out	⊂ In ∙ Out	● In ○ Out	● In ○ Out	All In All Out
Pullups	⊙ Off C On	€ Off C On	⊙ Off © On	⊙ Off © On	All Off All On
Out Set	1	1	0	0	Set
Out Value	0	1	0	0	
In Value	×	×	0	0	Get

Figure 20 : GPIO module when using I²C + GPIO

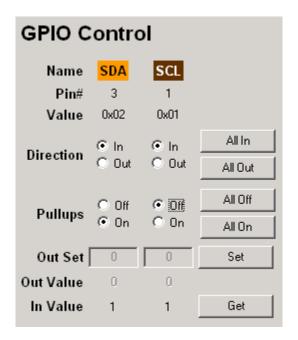


Figure 21 : GPIO module when using SPI + GPIO

4.3.2 GPIO Parameters

When GPIO module is selected, only the available pins are displayed in the window. Each pin is labeled and has parameters that can be set by the user.



Name

The name of each pin is color coordinated to match the color of the lead in of the 10-pin split cable adapter. The 10-pin split cable adapter is not included with the Aardvark adapter, but is available for purchase separately on the Total Phase website.

Pin

The position of the pin in the 10-pin socket connector. Information about the pin arrangement can be found the Aardvark I²C/SPI Embedded Systems Interface manual and on the Total Phase website.

Value

Each pin has a different value which can be OR-ed together to produce a single number that represents the state of all the pins.

The pins have the following values:

 $\begin{array}{rcl} SCL &=& 0 \times 01 \\ SDA &=& 0 \times 02 \\ MISO &=& 0 \times 04 \\ SCK &=& 0 \times 08 \\ MOSI &=& 0 \times 10 \\ SS &=& 0 \times 20 \end{array}$

For example, if SCL, MOSI and SS where set to 1, then the value of all pins would be:

 $0 \times 01 \mid 0 \times 10 \mid 0 \times 20 = 0 \times 31$

Direction (Dir.)

The direction of the pin, Input or Output.

If a pin is configured as an input pin, then the pullup selector and In Value row are enabled and the Out Set and Out Value rows are disabled.

If a pin is configured as an output pin, then the pullup selector and the In Value are disabled and the Out Set and Out Value rows are disabled.

All In and All Out



The "All In" and "All Out" buttons are convenience buttons to set all pins to input or output respectively.

Pull Ups (P.U.)

Indicates whether the pullup is active or inactive on a pin. The pullup selector is only enabled when the pin is set to the in direction because the pullup are only turned on on pins that have been configured as input. If a line is configured as output, the pullup mask is cached and the pullup configuration for that line will only take effect if the line is later configured as an input.

All On and All Off

The "All On" and "All Off" buttons are convenience buttons to turn on the pullups on or off respectively on all pins.

Note: Only pins that have been configured to be input will be affected by these buttons.

Note: I²C Pin Pullups

It is not possible to disable the pullups for SCL and SDA input on Hardware Version 1.02.

Out Set & Out Value

The **"Out Set"** boxes are a staging area for setting the levels of the output pins. Only **"0**" and **"1**" are accepted in these text boxes.

The values in the **"Out Set"** boxes are only applied once the **"Set** "button is clicked. The **"Out Value"** indicates the last known values of the output pins. After clicking the **"Set"** button the **"Out Set"** and the **"Out Value"** for all pins configured as output should match. The parameters of the transaction are added to the transaction log.

If a pin is switched from output to input, the values in "**Out Set**" and "**Out Value**" are disabled but are conserved. When a pin is switched back to output, these values will be restored.

In Value

The **"In Value"** is the last known values of the input pins. Initially the values for the In Value will be **"X"** indicating that the value is not known on the pin.

The **"In Value"** of the input pins are updated when the **"Get"** button is clicked. The parameters of the transaction are added to the transaction log.



The "In Value" for an output pin will always display an "X" because the input value of this pin is not known.

4.4 Batch Mode

Batch Mode		
Batch Instructions	_	
Kaardvark> 2 Koonfigure i2c="1" spi="1" gaio="0"/>		Stopped
(2c_bitste khz="400")> (2c_write add="0x38" count="0" radio="16"> 2c_write		Execute

Figure 22 : Batch Scripting Module

In Batch mode, the user can specify an arbitrary set of instructions for the Aardvark adapter to execute in sequence. This scripting language is based on XML.

4.4.1 Batch Instructions

A set of batch instructions for an Aardvark adapter is scripted in an XML based language. A set of instructions must be contained within a set of <aardvark> tags. Each command is specified by an XML tag. These tags are described in section *5. Batch Instruction Commands*. Commands are executed in the order that they appear in the XML block.

Help for the Batch Commands is available in the Control Center software which explains all the available commands. Just click on the **"Help"** button.





Figure 23 : Batch Commands Help System

4.4.2 Editing Batch Instructions

The Batch XML Instructions are entered in this text field. Batch instructions can be saved and loaded as XML files via the **"Save"** and **"Load"** buttons respectively. The **"Clear"** button will clear all contents out of the text field. The following dialog box will appear to confirm that the user wants to clear all data out of the text field.

Confirm	n	×		
Are you sure you want to clear all the I2C bus monitor data?				
	0k	Cancel		

Figure 24 : When clearing the batch command console, a dialog box will appear to confirm the user's action.

4.4.3 Executing Batch Instruction

To execute a set of batch instructions, simply click on the **"Execute"** button. When a script is executing, the status indicator will turn green. The results from the commands will appear in the transaction log. While a script is running, it is not possible to edit the batch instructions.

To stop the execution of a script, simply click on the "Stop" button.



Batch Mode Batch Instructions (deard/valk) coordigue (2c-11" spi="1" gpio="07/> c/2c_bites line="4007/> c/2c_bites line="4007/> c/2c_wite add="0.38" court="21" tade="16">01 isc/2c_wite> c/2c_wite add="0.38" cour

Figure 25 : Executing a batch command script.

Once the script has completed, the status indicator will change back to red and the batch instructions will once again be editable.

Batch Instruction Error

When the **"Execute"** button is clicked, the instruction set is parsed and validated. In the event of an error, a dialog box will open indicating the type of error and the command in which the error appears.

atch Instruction Error	×
There were enors in the Batch Instruction XML 0] configure (2c) - Invalid value. 0] configure (gpio) - Invalid value. 0] configure (power) - Invalid value. 0] configure (power) - Invalid value. 1] configure (public) - Invalid value. 1] configure (public) - Invalid value. 2] 2b; bhate (khc) - Invalid Value. 2] 2b; bhate (khc) - Invalid Bhate. 3] 2b; bhate (khc) - Invalid Bhate. 3] 2b; bhate (khc) - Invalid Bhate. 3] 2b; bhate (khc) - Invalid Bhate. 4] 2b; vinte (add) - Invalid Address 6] 2b; vinte (add) - Invalid Address 7] 2b; vinte (add) - Invalid Address	
0K	-

Figure 26 : Batch command error dialog.

The format of the error message is:

n) command [attribute] - error message

where:

n





zero-based index of the command

command

command type

attribute

attribute name where the error occurred. If an error is not associated with an attribute, this field will be omitted.

error message

error message.

Click OK to close the dialog.



5 Batch Instruction Commands

5.1 Notes on Batch Instructions

Unless specified otherwise, all arguments to a batch instruction are "1" to enable/request the specified parameter, or "0" to disable/not request the specified parameter.

The Batch Instruction commands support I2C master, SPI master, and GPIO modes. However, the batch Instruction commands do not support I2C slave and SPI slave mode.

5.2 General Commands

5.2.1 Configure

```
<configure i2c="i2c"
spi="spi"
gpio="gpio"
tpower="tpower"
pullups="pullups"/>
```

Activate/Deactivate individual sub-systems and features

Parameters

i2c	
	Enable I ² C.
spi	
	Enable SPI.
gpio	
	Enable GPIO.
tpower	
	Enable Target Power on pins 4 and 6.
pullups	
	Enable pullup resistors on the I ² C lines: SCL and SDA.



Details

It is not possible to activate I^2C , SPI and GPIO all at the same time. The possible combinations are:

 Table 2 : Allowed configuration combinations

i2c	spi	gpio	Configuration	
"1"	"1"	"0"	I ² C and SPI enabled	
"1"	"0"	"1"	I ² C enabled and SPI pins used as GPIO	
"0"	"1"	"1"	SPI enabled and I ² C pins used as GPIC	
"0"	"0"	"1"	I ² C and SPI pins used as GPIO	

Please see the Aardvark I²C/SPI Embedded Systems Interface Datasheet for more information about supplying target power and I²C pullup resistors.

5.2.2 sleep

<sleep ms="ms"/>

Set the Aardvark adapter to sleep for a number of milliseconds

Parameters

ms

Requested number of milliseconds to sleep.

5.3 I²C Commands

5.3.1 i2c_bitrate

<i2c_bitrate khz="khz"/>

Set the l^2C bitrate in kilohertz.

Parameters

khz



The requested bitrate in kHz.

Details

The default power-on bitrate is 100khz

Only certain discrete bitrates are supported by the Aardvark I²C master interface. As such, this actual bitrate set will be less than or equal to the requested bitrate.

The actual bitrate is returned in the Transaction Log. Please see the Aardvark I²C/SPI Embedded Systems Interface Datasheet for more information.

5.3.2 i2c_write

Write a stream of bytes to the I²C slave device.

Parameters

addr

The slave from which to read. The slave address can be specified in decimal or hexadecimal notation.

count

The number of bytes to write (maximum 65535).

nostop

Request that no stop condition is issued on the I²C bus after the transaction completes.

ten_bit_addr

Request that the provided address is treated as a 10-bit address.

combined_fmt



Request that the Philips combined format is followed during a I²C read operation. This only has an effect when used in conjunction with 10-bit addressing.

radix

The base of the number system of the message, with the value being 10 for decimal, or 16 for hexadecimal.

message

The message to transmit as a space-separated list of numbers.

Details

For ordinary 7-bit addressing, the lower 7 bits of the addr should correspond to the slave address. The topmost bits are ignored. The Aardvark I²C subsystem will assemble the address along with the R/W bit after grabbing the bus. For 10- bit addressing, the lower 10 bits of addr should correspond to the slave address. The Aardvark adapter will then assemble the address into the proper format as described in the Philips specification. There is a limitation that a maximum of only 65534 bytes can be written in a single transaction if the 10-bit addressing mode is used.

The slave_addr 0x00 has been reserved in the I²C protocol specification for general call addressing. I²C slaves that are enabled to respond to a general call will acknowledge this address. The general call is not treated specially in the Aardvark I²C master. The user of this API can manually assemble the first data byte if the hardware address programming feature with general call is required.

Please see the Aardvark I²C/SPI Embedded Systems Interface Datasheet for more information.

5.3.3 i2c_read

```
<i2c_read addr="addr"
count="count"
nostop="nostop"
ten_bit_addr="ten_bit_addr"
combined_fmt="combined_fmt"/>
```

Read a stream of bytes from the I²C slave device.

Parameters

addr



The slave from which to read. The slave address can be specified in decimal or hexadecimal notation.

count

The number of bytes to read (maximum 65535).

nostop

Request that no stop condition is issued on the I²C bus after the transaction completes.

ten_bit_addr

Request that the provided address is treated as a 10-bit address.

combined_fmt

Request that the Philips combined format is followed during a I²C read operation. This only has an effect when used in conjunction with 10-bit addressing.

Details

For ordinary 7-bit addressing, the lower 7 bits of the addr should correspond to the slave address. The topmost bits are ignored. The Aardvark I²C subsystem will assemble the address along with the R/W bit after grabbing the bus. For 10-bit addressing, the lower 10 bits of addr should correspond to the slave address. The Aardvark adapter will then assemble the address into the proper format as described in the Philips specification, namely by first issuing an write transaction on the bus to specify the 10-bit slave and then a read transaction to read the requested number of bytes. The initial write transaction can be skipped if the Combined Format feature is requested in conjunction with the 10-bit addressing functionality.

Please see the Aardvark I²C/SPI Embedded Systems Interface Datasheet for more information.

5.3.4 i2c_free_bus

<i2c_free_bus/>

Free the Aardvark I²C subsystem from a held bus condition (e.g., no stop).

Parameters

None



Details

If the Aardvark I²C subsystem had executed a master transaction and is holding the bus due to a previous nostop flag, this function will issue the stop command and free the bus.

Please see the Aardvark I²C/SPI Embedded Systems Interface Datasheet for more information.

5.4 SPI Commands

5.4.1 spi_config

```
<spi_config polarity="polarity"
    phase="phase"
    bitorder="bitorder"
    ss="ss"/>
```

Configure the SPI master interface.

Parameters

polarity

Set as either "rising/falling" or "falling/rising".

phase

Set as either "sample/setup" or "setup/sample".

bitorder

Set as either "msb" or "lsb".

SS

Set as either "active_low" or "active_high".

Details

These configuration parameters specify how to clock the bits that are sent and received on the Aardvark SPI interface.

The polarity option specifies which transition constitutes the leading edge and which transition is the falling edge. For example, rising/falling would configure the SPI to idle



the SCLK clock line low. The clock would then transition low-to-high on the leading edge and high-to-low on the trailing edge.

The phase option determines whether to sample or setup on the leading edge. For example, "sample/setup" would configure the SPI to sample on the leading edge and setup on the trailing edge. The bitorder option is used to indicate whether LSB or MSB is shifted first.

The ss option is used change the output polarity on the SS line. For example, "active_low" will pull the SS line low to active the slave device.

Please see the Aardvark I²C/SPI Embedded Systems Interface Datasheet for more information.

5.4.2 spi_bitrate

<spi_bitrate khz="khz"/>

Set the SPI bitrate in kilohertz.

Parameters

khz

The requested bitrate in kHz.

Details

The power-on default bitrate is 1000 kHz. Only certain discrete bitrates are supported by the Aardvark adapter. As such, this actual bitrate set will be less than or equal to the requested bitrate unless the requested value is less than 125 kHz, in which case the Aardvark adapter will default to 125 kHz.

The actual bitrate is returned in the Transaction Log.

Please see the Aardvark I²C/SPI Embedded Systems Interface Datasheet for more information.

5.4.3 spi_write



Write a stream of bytes to the downstream SPI slave device and read back the fullduplex response.

Parameters

count	
	The number of bytes to write (maximum 65535).
radix	
	The base of the number system of the message, with the value being 10 for decimal, or 16 for hexadecimal.
message	
	The message to transmit as a space separated list of numbers.

Details

If count is 0, no bytes will be written to the slave. However, the slave select line will be dropped for 5-10 microseconds. This can be useful in sending a signal to a downstream SPI slave without actually sending any bytes. For example, if an SPI slave has tied the slave select to an interrupt line and it sees the line is toggled without any bytes sent, it can interpret the action as a command to prepare its firmware for an subsequent reception of bytes.

Please see the Aardvark I²C/SPI Embedded Systems Interface Datasheet for more information.

5.5 GPIO Commands

The following table maps the named lines on the Aardvark I²C/SPI output cable to bit positions in the direction and pullups masks. All GPIO API functions will index these lines through a single 8-bit masked value. Thus, each bit position in the mask can be referred back its corresponding line through the mapping described below.

Table 3 : GPIO Bit Values

Pin #	Bit Value	Description
Pin 1	0x01	I ² C SCL line
Pin 3	0x02	I ² C SDA line
Pin 5	0x04	SPI MISO line
Pin 7	0x08	SPI SCK line



Pin 8	0x10	SPI MOSI line
Pin 9	0x20	SPI SS line

5.5.1 gpio_config

Configure the GPIO interface.

Parameters

direction

A single byte value where each bit corresponds to the physical line as defined in Table 3. If a line's bit is 0, the line is configured as an input. Otherwise it will be an output.

pullups

A single byte value where each bit corresponds to the physical line as defined in Table 3. If a line's bit is 1, the lines pullup is active whenever the line is configured as an input. Otherwise the pullup will be deactivated.

Details

Please see the Aardvark I $^2C/SPI$ Embedded Systems Interface Datasheet for more information.

5.5.2 gpio_get

<gpio_get/>

Get the value of current GPIO inputs.

Parameters

None



Details

A line's bit position in the mask will be 0 if it is configured as an output or if it corresponds to a subsystem that is still active.

Please see the Aardvark I²C/SPI Embedded Systems Interface Datasheet for more information.

5.5.3 gpio_set

<gpio_set value="value"/>

Set the value of current GPIO outputs.

Parameters

value

A bitmask as defined in Table 3 specifying which outputs should be set to logic high and which should be set to logic low.

Details

If a line is configured as an input or not activated for GPIO, the output value will be cached. The next time the line is an output and activated for GPIO, the output value previously set will automatically take effect.

Please see the Aardvark I²C/SPI Embedded Systems Interface Datasheet for more information.



6 Notes

6.1 Multiple Units

It is possible to operate multiple Aardvark adapters simultaneously. Each window of the application is bound to a single Aardvark adapter. Additional Aardvark units can be accessed by opening additional windows.

To open a new Window simply go to the menu item: File | New Window.

A new window will open which will need to be configured to the additional Aardvark device.

6.2 Aardvark I²C/SPI Host Adapter Technical Specifications

Detailed information about the Technical Specifications of the Aardvark I²C/SPI Host Adapter can be found on the Total Phase website: http://www.totalphase.com/



7 Legal / Contact

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