

Next-Generation Platform for Accelerated Computing



The Terasic Mercury A2700 accelerator card leverages the Intel industry's highest performance Agilex®7 I-Series FPGA with 2700K logic elements to address the most compute and bandwidth-demanding applications in the data center, in the cloud, and in embedded devices.



As the first Terasic accelerator that provides PCle 5.0 x16 and Compute Express Link (CXL) support, the Mercury A2700 accelerator card enables 2X higher bandwidth compared with PCle 4.0 interface for higher data throughput, as well as high-speed, low-latency, and efficient performance between CPU and FPGA.



In addition, armed with two 200G QSFP-DD connectors, and four DDR4 SO-DIMM sockets, the Mercury A2700 accelerator card accelerates every workload across the data center and edge in computer vision, high performance computing, and other compute-intensive applications.

Key Benefits \mathcal{Q}



FPGA

Intel® Agilex® 7 I-Series FPGA with 2700K Logic Elements. Core Speed Grade: -2 / XCVR Speed Grade: -1 or -2



QSFP-DD

Two QSFP-DD connectors for 200/100/40/25/10 GbE network interface



DDR4

Four DDR4 SO-DIMM sockets with error correction code (ECC) for both FPGA and HPS fabric



PCI Express Gen 5.0 x16 and CXL x16 (Note*) with full-height, 3/4-length form-factor package

MCIO

Two MCIO 8x connectors to support PCIe and CXL (Note*)

HPS Interface

HPS communication interface with USB to UART, MicroSD socket, Gigabit PHY, and USB OTG



Drive breakthrough performance with the new Terasic Mercury A2700 Accelerator Card!

Targeted Applications

- · Data center acceleration
- · Al and deep learning
- · High-performance computing
- High-end test and measurement
- Medical
- Communications

Block Diagram \wp







